

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leonardi Salvatore  
 Filed : December 28, 2001  
 For : INTEGRATION PROCESS ON A SOI SUBSTRATE OF  
 A SEMICONDUCTOR DEVICE COMPRISING AT  
 LEAST A DIELECTRICALLY ISOLATED WELL

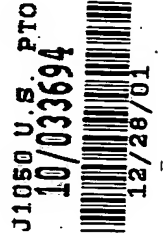
Docket No. : 856063.721  
 Date : December 28, 2001

Box Patent Application  
 Commissioner for Patents  
 Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicant does not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserves the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

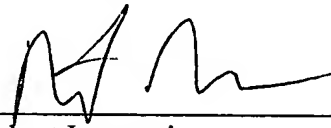


Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

Leonardi Salvatore

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Enclosures:

Form PTO-1449

Cited References (6)

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